



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**ATTORNEY DOCKET NO. 025311-0105**

Applicants: Ichiro OKABE et al.

Title: METHOD OF FORMING A FINE PATTERN USING A SILICON-  
OXIDE-BASED FILM, SEMICONDUCTOR DEVICE WITH A  
SILICON-OXIDE-BASED FILM AND METHOD OF  
MANUFACTURE THEREOF

Patent No.: 6,992,013

Issue Date: 01/31/2006

Examiner: Jose R. Diaz

Art Unit: 2815

**PETITION TO CORRECT NAME OF ASSIGNEE ON**  
**AN ISSUED PATENT UNDER 37 C.F.R. § 1.183**

Mail Stop Petitions  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Patentees respectfully request that 37 C.F.R. §3.81(a) be waived to permit the correct name of the assignee to be provided after issuance for the above-identified patent, 6,992,013.

Applicants hereby state that the failure to include the correct assignee name on Form PTOL-85B was inadvertent and enclose herewith a copy of the Notice of Recordation of Assignment Document and a Certificate of Correction Form PTO/SB/44 (in duplicate) indicating the correction to be made.

A credit card payment form in the amount of \$130.00 is enclosed for the amount stated in 37 C.F.R. § 1.17(h) to cover the fee for this petition.

05/01/2006 CNGUYEN2 00000093 09597161

01 FC:1464

130.00 OP

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If the Request is granted, please forward the file to Certificates of Correction Branch, for issuance of a Certificate of Correction.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §1.16-§1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Respectfully submitted,

April 27, 2006


Date

FOLEY & LARDNER, LLP

Customer Number: 22428

Telephone: (202) 672-5300

Facsimile: (202) 672-5399



William T. Ellis

Attorney for Applicants

Registration No. 26,874

**UNITED STATES PATENT AND TRADEMARK OFFICE**UNDER SECRETARY OF COMMERCE FOR INTELLECTUAL PROPERTY AND  
DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE

MARCH 08, 2006

PTAS

**\*700244578A\*****\*700244578\***

WILLIAM T. ELLIS  
FOLEY & LARDNER LLP  
WASHINGTON HARBOUR  
3000 K STREET, N.W., SUITE 500  
WASHINGTON, D.C. 20007-5143

**UNITED STATES PATENT AND TRADEMARK OFFICE  
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PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 571-272-3350. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, MAIL STOP: ASSIGNMENT SERVICES BRANCH, P.O. BOX 1450, ALEXANDRIA, VA 22313.

RECORDATION DATE: 02/16/2006

REEL/FRAME: 017262/0569

NUMBER OF PAGES: 8

BRIEF: ASSIGNEE'S ADDRESS CORRECTION

## ASSIGNOR:

SEMICONDUCTOR LEADING EDGE  
TECHNOLOGIES, INC.

DOC DATE: 09/15/2005

## ASSIGNEE:

SHARP KABUSHIKI KAISHA  
22-22, NAGAIKE, ABENO-KU, OSAKA-  
SHI  
OSAKA 545-8522, JAPAN

SERIAL NUMBER: 09597161

FILING DATE: 06/20/2000

PATENT NUMBER: 6992013

ISSUE DATE: 01/31/2006

TITLE: METHOD OF FORMING A FINE PATTERN USING A SILICON-OXIDE-BASED FILM,  
SEMICONDUCTOR DEVICE WITH A SILICON-OXIDE-BASED FILM AND METHOD OF  
MANUFACTURE THEREOF

017262/0569 PAGE 2

SERIAL NUMBER: 09584752

FILING DATE: 06/01/2000

PATENT NUMBER: 6446645

ISSUE DATE: 09/10/2002

TITLE: DIP CLEANING SYSTEM FOR A SUBSTRATE, SYSTEM FOR EVALUATING THE  
AMOUNT OF PARTICLES AND METHOD OF EVALUATING THE AMOUNT OF  
PARTICLES ADHERING TO A SUBSTRATE

SERIAL NUMBER: 09587359

FILING DATE: 06/02/2000

PATENT NUMBER: 6586163

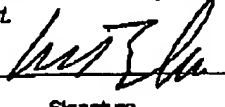
ISSUE DATE: 07/01/2003

TITLE: METHOD OF FORMING FINE PATTERN

STEVEN POST, EXAMINER  
ASSIGNMENT SERVICES BRANCH  
PUBLIC RECORDS DIVISION

02/10/2006  
700244578

Atty. Dkt. No. 025311-0123

FORM PTO-1695 (modified) (Rev. 5-05)		U.S. DEPARTMENT OF COMMERCE Patent and Trademark Office			
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To the Director of the United States Patent and Trademark Office: Please record the attached original documents or copies thereof.					
1. Name of conveying party(ies):  <b>Semiconductor Leading Edge Technologies, Inc.</b>  Additional conveying party(ies) <b>NO</b>		2. Name and address of receiving party(ies):  <b>Sharp Kabushiki Kaisha 22-22, Nagaike, Abeno-ku, Osaka-shi Osaka, 545-8522 JAPAN</b>			
3. Nature of conveyance: <b>ASSIGNEE'S ADDRESS CORRECTION</b> <b>Reel/Frame 016891/0741</b>  Execution Date: <b>September 15, 2005</b>		Additional name(s) & address(es) attached? <b>NO</b>			
4. Application number(s) or patent number(s): If this is being filed together with a new application, the execution date of the application is: <table border="1" style="width: 100%;"> <tr> <td style="width: 50%;">           A. Patent Application Number(s):   <b>09/597,161</b> </td> <td style="width: 50%;">           B. Patent Number(s):   <b>6,446,645 6,586,163</b> </td> </tr> </table> Additional numbers attached? <b>NO</b>				A. Patent Application Number(s):  <b>09/597,161</b>	B. Patent Number(s):  <b>6,446,645 6,586,163</b>
A. Patent Application Number(s):  <b>09/597,161</b>	B. Patent Number(s):  <b>6,446,645 6,586,163</b>				
5. Name and address of party to whom correspondence concerning document should be FAXED TO:  <b>William T. Ellis FOLEY &amp; LARDNER LLP Washington Harbour 3000 K Street, N.W., Suite 500 Washington, D.C. 20007-5143 Fax No.: 202-672-5399</b>		6. Total number of applications/patents involved: <b>3</b> 7. Total fee (37 C.F.R. § 3.41): Check Enclosed <input checked="" type="checkbox"/> Charge to deposit account 8. Deposit account number: <b>19-0741</b>			
DO NOT USE THIS SPACE					
9. Statement and signature: <i>To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. The Commissioner is hereby authorized to charge any additional recordation fees which may be required in this matter to the above-identified deposit account.</i>					
<b>William T. Ellis</b> Name of person signing		 Signature			
		<b>February 16, 2006</b> Date			
Total number of pages including cover sheet, attachments, and document: <b>6</b>					

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(Also Form PTO-1050)

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

PATENT NO. : 6,992,013  
APPLICATION NO. : 09/597,161  
DATED : 01/31/2006  
INVENTOR(S) : Ichiro OKABE; Hiroki ARAI

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

**ON THE FRONT OF PATENT**

(73) Assignees: Delete "Semiconductor Leading Edge Technologies, Inc." and replace it with -Sharp Kabushiki Kaisha--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Washington Harbour  
3000 K Street, N.W., Suite 500  
Washington, D.C. 20007-5143

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(Also Form PTO-1050)

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